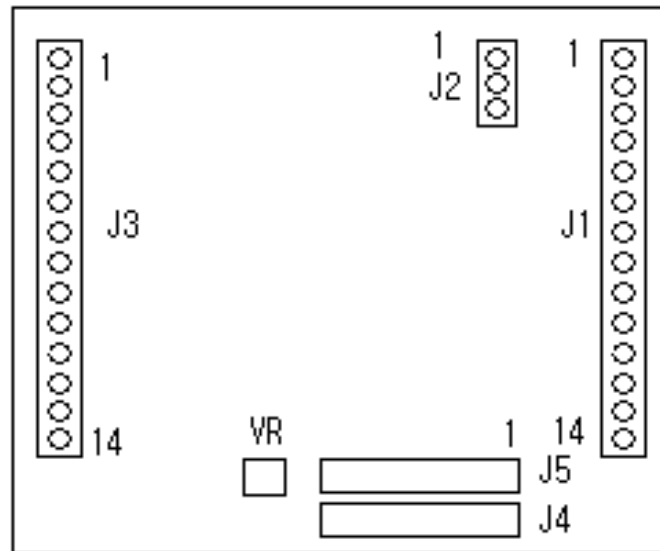


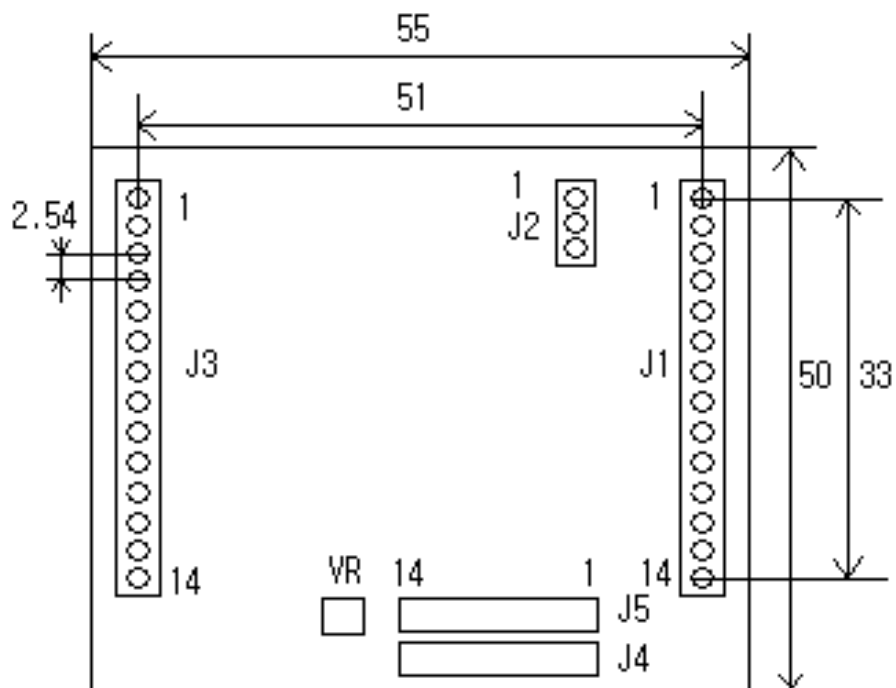
MSGO335A V1.02

1.



1. MSGO335A V1.02

: 55 * 50 [mm]



2. MSGO335A V1.02 Outline Dimensions

2.

- : +5[VDC]
- S1D13305F00A LCD Controller
- Display Memory 32[Kbyte]
- Negative LCD Bias Voltage [VEE-VCC] = 28[VDC]
[VO-VCC] = 14 ~ 28[VDC]
- LCD Contrast 가
- 8080/6800 Interface

3. MSGO335A Connector

3.1) J1 Pin

3-1. J1 Pin

Connector NO.	Pin Name		
1	VCC	+5[VDC]	
2	Open	Open	
3	A0	Address Line	
4	/SED_CS	Chip Select	Note 1.1)
5	WR	Data Write	Note 1.2)
6	RD	Data Read	Note 1.3)
7	/RST	Reset	
8	Open	Open	
9	Open	Open	
10	Open	Open	
11	Open	Open	
12	FGND	Frame GND	
13	Open	Open	
14	GND	Signal GND	

Note1.

- 1.1) 8080 Interface Read Signal [/RD]
- 6800 Interface Enable Clock [E]
- 1.2) 8080 Interface Write Signal [/WR]
- 6800 Interface R/W Signal [R//W]
- ' SED1335 Technical Manual 5.2.3 ' .

1.3) SED1335 Chip Select.

3.2) J3 Pin

3-2. J3 Pin

Connector NO.	Pin Name		
1	Open	Open	
2	VCC	+5[VDC]	
3	D0	Data Line	
4	D1	Data Line	
5	D2	Data Line	
6	D3	Data Line	
7	D4	Data Line	
8	D5	Data Line	
9	D6	Data Line	
10	D7	Data Line	
11	Open	Open	
12	Open	Open	
13	Open	Open	
14	GND	Signal GND	

3.3) J2 Pin

3-3. J2 Pin

Connector NO.	Pin Name		
1	VCC	+5[VDC]	
2	SEL1	SEL1	Note 2.1)
3	GND	Signal GND	

Note2.

2.1) SEL1 VCC => 6800 Interface

SEL1 GND => 8080 Interface

' SED1335 Technical Manual 5.2.3 ' .

3.4) J4 Pin

3-4. J4 Pin

Connector NO.	Pin Name		
1	VD0	Display Data	
2	VD1	Display Data	
3	VD2	Display Data	
4	VD3	Display Data	
5	LCDEN	Display ON/OFF	
6	FLM	Frame Pulse	
7	M	Note 3.1)	
8	CL1	Latch Pulse	
9	CL2	Shift Clock	
10	VCC	+5[VDC]	
11	GND	Signal GND	
12	VEE	VEE	
13	VO	VO	
14	FGND	Frame GND	

3.5) J5 Pin

3-5. J5 Pin

Connector NO.	Pin Name		
1	FLM	Frame Pulse	
2	M	Note 3.1)	
3	CL1	Latch Pulse	
4	CL2	Shift Clock	
5	LCDEN	Display ON/OFF	
6	VD0	Display Data	
7	VD1	Display Data	
8	VD2	Display Data	
9	VD3	Display Data	
10	VCC	+5[VDC]	
11	GND	Signal GND	
12	VEE	VEE	
13	VO	VO	
14	FGND	Frame GND	

Note 3.

3.1) M ⇒ Alternator for LCD drive

3 48-6

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